S50 TEST SYSTEM
THE SUPERLATIVE 50MHZ TESTER

KEY FEATURES

■ **64 to 256 Logic I/O pins**

✔ with 64 pin increments

■ **Up to 32 sites parallel testing**

✔ Simultaneous of up to 32 DUTs

✔ Any pin, any resource can be assigned to any site

■ **Logic / Mixed-Signal / Analog Options**

■ **Cable Mount Solution**

A COST-EFFECTIVE PLATFORM FOR LOGIC / MIXED-SIGNAL / ANALOG IC TEST

APPLICATIONS

✔ application specific test solution
✔ test configurable solution
✔ low cost test solution
✔ mass production proven solution

FUNCTIONS

■ **Logic & APG Board**

✔ **50MHz**

✔ **TG and Levels Per Pin, 64 Logic I/O pins**

✔ **32 Timing Set and 32 Format Set change on-the-fly**

✔ **16X, 16Y, 16D Algorithmic Pattern Generation**

✔ **+11V/-2V, 35mA driving capability**

✔ **2 DPS, 8 PMU, 64 PPMU, TMU, DVM**

✔ **17M local memory**

■ **Mixed-Signal Board**

✔ **+40V, -15V / ± 1.8A V/I Source/Meter x8**

✔ **500KSPS / 16 Bits AWG / Digitizer**

✔ **RMS, DVM , TMU , Data Acquisition Unit,**

✔ **32 User Relay**

■ **OPM Board**

✔ **64 Channels, Force and Sense Per Channel**

✔ **+40V, -15V / ± 1.8A V/I Source / Meter x8**

✔ **4 DVM ,TMU, 32 User Relay**

YOUNGTEK ELECTRONICS CORP.